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CASE DOCKET NO. YOR920010768US2Date: January 02, 2004Division of: 10/207,352S:
P.T.O:

Transmitted herewith for filing is the Patent Application of:
 Inventors: Dinkar Singh et al.

For: **Enhanced T-gate Structure for Modulation Doped Field Effect Transistors**

Enclosed are:

Express Mail Label No.: ER459919465USDate Deposited: January 02, 2004 5 Sheets of formal Drawings. This application is filed Under Rule 1.53(b)

The filing fee has been calculated as shown below:

FOR:	(Col. 1) NO. FILED	(Col. 2) NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	13 - 20 =	0
INDEP CLAIMS	3 - 3 =	0
<u>_0_ MULTIPLE DEPENDENT CLAIM PRESENTED</u>		

If the difference in Col. 1 is less than zero, enter "0" in Col. 2.

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Any additional filing fees required under 37 CFR 1.16.

Any patent application processing fees under 35 CFR 1.17.

Respectfully submitted,

By George Sai-Halasz
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Atty. Docket No. YOR920010768US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Dinkar Singh et al.

Serial No.:

Group Art Unit:

Filed: herewith

Examiner:

For: **ENHANCED T-GATE STRUCTURE FOR MODULATION DOPED FIELD EFFECT TRANSISTORS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

EXPRESS MAIL CERTIFICATE

"Express Mail" label number: ER459919465US

Date of Deposit: January 02, 2004

I hereby certify that the following **attached** paper or fee

Patent Application (25 sheets) filed under Rule 1.53(b)
Five (5) sheets of formal drawings
Declaration and Power of Attorney (2 pages)
Associate Power of Attorney
Application Data Sheet (4 pages)
Preliminary Amendment (7 pages)
Patent Transmittal Sheet (in duplicate)
Express mail certificate
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is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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